



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 10/660,709 Confirmation No. 7653
Applicant : Y. KAWAMURA et al
Filed : September 12, 2003
Title : METHOD FOR POLISHING SURFACE OF SEMICONDUCTOR DEVICE SUBSTRATE
TC/AU : 1763
Examiner : G.A. Goudreau
Docket No. : ASA-955-02
Customer No.: 24956

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PETITION FOR EXTENSION OF TIME

Sir:

It is respectfully requested that a three-month Extension of Time, to and including April 7, 2006, be granted in which to respond to the Office Action dated October 7, 2005 in the above-identified application.

Our Credit Card Payment Form in the amount of **\$1020.00** is attached in payment of the appropriate fee.

The Commissioner is hereby authorized to charge any additional payment due, or to credit any overpayment, to Deposit Account No. 50-1417.

Respectfully submitted,

MATTINGLY, STANGER, MALUR & BRUNDIDGE, P.C.

04/04/2006 JADD01 00000117 10660709

1020.00 0P

By _____
Shrinath Malur
Reg. No. 34,663
(703) 684-1120

April 3, 2006